

Sir:

Transmitted herewith for filing is the Patent Application of:

Inventor: YEEN TAT CHAN, KHENG CHOK TEE, YIANG AUN NGA, ZHAO LUN, WANG LING GOH, DIING SHEN PAN

METHOD OF FORMING A PARTIALLY DEPLETED SILICON ON INSULATOR (PDSOI) TRANSISTOR WITH A PAD

LOCK BODY EXTENSION

Enclosed are:

- ☒ 5 sheets of drawing(s) - formal.
☒ An assignment of the invention to **Chartered Semiconductor Manufacturing Ltd.**
☐ An associate power of attorney ☐ Applicant claims small entity status
☐ Request & Certification under 35 USC 122(b)(2)(b)(i)

The filing fee has been calculated as shown below:

	(Col. 1)	(Col. 2)	OTHER THAN A SMALL ENTITY	
FOR:	NO. FILED	*NO. EXTRA	RATE	FEE
BASIC FEE				\$ 750.
TOTAL CLAIMS	44 -20=	24	x 18 =	\$ 432.
INDEP CLAIMS	4 -3=	1	x 84 =	\$ 84.
SUB TOTAL				\$ 1,266.
ASSIGNMENT				\$ 40.
TOTAL				\$ 1,306.

- ☒ Please charge my Deposit Account No. 19-0033 in the amount of **\$ 1,306**. A duplicate copy of this sheet is enclosed.
☒ The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. 19-0033. A duplicate copy of this sheet is enclosed.
☒ Any additional filing fees required under 37 CFR §1.16.
☒ Any patent application processing fees under 37 CFR §1.17.

Respectfully submitted,

STEPHEN S. ACKERMAN, REG. NO. 37,761

EXPRESS MAIL CERTIFICATE

Express Mail No. **EV313928154US**

I hereby certify that this correspondence is being deposited with the United States Postal Service as Express Mail in an envelope addressed to Assistant Commissioner of Patents, Alexandria, VA, 22313-1450. Applicant and/or Attorney requests the date of deposit as the Filing Date.

7/29/03
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